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L3: Entry 4 of 5   File: DWPI   Mar 15, 2001

DERWENT-ACC-NO: 1999-095976
DERWENT-WEEK: 200159
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TITLE: <u>Scaling and flicker</u> reduction system for displaying PC
signals on television - has conditional <u>scaling</u> circuit which
applies <u>scaling</u> factor to each pixel depending on pixel conditions,
and adaptive <u>flicker</u> reduction circuit which uses two dimensional
window around pixel to select <u>flicker filter</u>

INVENTOR: KE, L; LUTZ, J M

PATENT-ASSIGNEE:
ASSIGNEE   CODE
CIRRUS LOGIC INC   CIRRN

PRIORITY-DATA: 1997US-0886113 (June 30, 1997), 1999US-035152 (July
12, 1999)

PATENT-FAMILY:

PUB-NO	PUB-DATE	LANGUAGE	PAGES	MAIN-IPC
KR 2001020520 A	March 15, 2001		000	G09G00L/16
WO 9900785 A1	January 7, 1999	E	044	G09G00L/16
AU 9885674 A	January 19, 1999		000	
US 5963262 A	October 5, 1999		000	H04N007/01
EP 1016060 A1	July 5, 2000	E	000	G09G00L/16
US 6094226 A	July 25, 2000		000	H04N007/01

DESIGNATED-STATES: AL AU BA BB BR CA CN CU CZ EE GE GH GM GW HU ID
IL IS JP KE KP KR LC LK LR LS LT LV MG MK MN MW MX NO NZ PL RO SD
SG SI SK SL TR TT UG VN YU ZW AT BE CH CY DE DK EA ES FI FR GB GR
IE IT LU MC NL PT SE DE FR GB IE IT NL

APPLICATION-DATA:

PUB-NO	APPL-DATE	APPL-NO	DESCRIPTOR
KR2001020520A	December 24, 1999	1999KR-0712311	
WO 9900785A1	June 18, 1998	1998WO-US12816	
AU 9885674A	June 18, 1998	1998AU-0085674	
AU 9885674A		WO 9900785	Based on
US 5963262A	June 30, 1997	1997US-0886113	
EP 1016060A1	June 18, 1998	1998EP-0936808	
EP 1016060A1	June 18, 1998	1998WO-US12816	
EP 1016060A1		WO 9900785	Based on
US 6094226A	June 30, 1997	1997US-0886113	Cont of
US 6094226A	July 12, 1999	1999US-0351552	
US 6094226A		US 5963262	Cont of

INT-CL (IPC): <u>G09</u> G 1/<u>16</u>; <u>H04</u> N 7/<u>01</u>

ABSTRACTED-PUB-NO: US 5963262A
BASIC-ABSTRACT:

The <u>scaling and flicker</u> reduction system for conversion of
non-interlaced images to a different resolution interlaced format
includes conditional <u>scaling</u> circuitry which conditionally applies
a <u>scaling</u> factor to each pixel within an image dependent upon pixel
conditions including each pixel. An adaptive <u>flicker</u> reduction
circuit receives <u>scaled</u> image data from the <u>scaling</u> circuitry and
uses a two-dimensional window around each pixel within the <u>scaled</u>
image data to select one of several <u>flicker</u> reduction <u>filters</u> for
each pixel depending upon pixel conditions within the
two-dimensional window.

The <u>filter</u> receives data which represents pixel values from at
least two lines of an image. The <u>filter</u> provides at least two
frequency responses.

ADVANTAGE - Allows <u>scaling</u> of images from one resolution to another
without needing large silicon area. Improves handling of <u>flicker</u> in
mixed images with text, <u>graphics</u> and continuous tone signals. Takes
into account horizontal as well as vertical pixel conditions.
ABSTRACTED-PUB-NO:

US 6094226A
EQUIVALENT-ABSTRACTS:

The <u>scaling and flicker</u> reduction system for conversion of
non-interlaced images to a different resolution interlaced format
includes conditional <u>scaling</u> circuitry which conditionally applies
a <u>scaling</u> factor to each pixel within an image dependent upon pixel
conditions including each pixel. An adaptive <u>flicker</u> reduction
circuit receives <u>scaled</u> image data from the <u>scaling</u> circuitry and
uses a two-dimensional window around each pixel within the <u>scaled</u>
image data to select one of several <u>flicker</u> reduction <u>filters</u> for
each pixel depending upon pixel conditions within the
two-dimensional window.

The _filter_ receives data which represents pixel values from at least two lines of an image. The _filter_ provides at least two frequency responses.

ADVANTAGE - Allows _scaling_ of images from one resolution to another without needing large silicon area. Improves handling of _flicker_ in mixed images with text, _graphics_ and continuous tone signals. Takes into account horizontal as well as vertical pixel conditions.

The _scaling and flicker_ reduction system for conversion of non-interlaced images to a different resolution interlaced format includes conditional _scaling_ circuitry which conditionally applies a _scaling_ factor to each pixel within an image dependent upon pixel conditions including each pixel. An adaptive _flicker_ reduction circuit receives _scaled_ image data from the _scaling_ circuitry and uses a two-dimensional window around each pixel within the _scaled_ image data to select one of several _flicker_ reduction _filters_ for each pixel depending upon pixel conditions within the two-dimensional window.

The _filter_ receives data which represents pixel values from at least two lines of an image. The _filter_ provides at least two frequency responses.

ADVANTAGE - Allows _scaling_ of images from one resolution to another without needing large silicon area. Improves handling of _flicker_ in mixed images with text, _graphics_ and continuous tone signals. Takes into account horizontal as well as vertical pixel conditions.

WO 9900785A

CHOSEN-DRAWING: Dwg.1B/6

TITLE-TERMS: _SCALE FLICKER_ REDUCE SYSTEM DISPLAY SIGNAL TELEVISION CONDITION _SCALE_ CIRCUIT APPLY _SCALE_ FACTOR PIXEL DEPEND PIXEL CONDITION ADAPT _FLICKER_ REDUCE CIRCUIT TWO DIMENSION WINDOW PIXEL SELECT _FLICKER FILTER_

DERWENT-CLASS: P85 T04 W03

EPI-CODES: T04-H03; W03-A04H; W03-A11A; W03-A18C;

SECONDARY-ACC-NO:
Non-CPI Secondary Accession Numbers: N1999-069748

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L3: Entry 2 of 5   File: EPAB   Jan 7, 1999

PUB-NO: WO009900785A1
DOCUMENT-IDENTIFIER: WO 9900785 A1
TITLE: SYSTEM AND METHOD FOR CONVERSION OF PROGRESSIVE SCANNED
IMAGES TO TELEVISION INPUT FORMATS

PUBN-DATE: January 7, 1999

INVENTOR-INFORMATION:

NAME	COUNTRY
KE, LIGANG	
LUTZ, JUERGEN M	

ASSIGNEE-INFORMATION:

NAME	COUNTRY
CIRRUS LOGIC INC	US

APPL-NO: US09812816
APPL-DATE: June 18, 1998

PRIORITY-DATA: US88611397A (June 27, 1997)

INT-CL (IPC): G09 G 1/16
EUR-CL (EPC): G09G005/04; H04N009/64

ABSTRACT:

A system and method for conversion of graphics from computer
graphics formats to television formats are disclosed. More
particularly, an improved scaling and flicker reduction system and
method are disclosed for scaling personal computer (PC) graphics
formats into different resolution television (TV) formats and for
reducing flicker due to the conversion process of interlacing
non-interlaced PC graphics to match interlaced TV formats. The
scaling implementation reduces line buffer requirements by using a
conditional scaling technique for converting graphics from a PC
resolution format to a TV resolution format. The flicker reduction
implementation provides a two-dimensional adaptive filter that
selects between multiple filters so that different parts of an
image may have different flicker reduction and different levels of
trade off between flicker reduction and resolution. The system and
method disclosed are more generally applicable to converting images
from one resolution to another and to reducing flicker in images
converted from a non-interlaced format to an interlaced format.

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L3: Entry 1 of 5   File: JPAB   Nov 17, 1998

PUB-NO: JP410308926A
DOCUMENT-IDENTIFIER: JP 10308926 A
TITLE: COMPUTER SYSTEM

PUBN-DATE: November 17, 1998

INVENTOR-INFORMATION:
NAME   COUNTRY
INO, TAKAO

ASSIGNEE-INFORMATION:
NAME   COUNTRY
TOSHIBA CORP

APPL-NO: JP09119833
APPL-DATE: May 9, 1997

INT-CL (IPC): H04 N 5/92; H04 N 5/85

ABSTRACT:

PROBLEM TO BE SOLVED: To provide a computer system that provides a
reproduced image with high image quality without deteriorating
image quality due to an encoder at DVD reproduction, in the
computer system provided with the encoder that outputs a DVD
reproduction image onto a television receiver.

SOLUTION: A selector 13 receives a digital RGB signal outputted
from a graphic controller and an RGB signal via a scaling circuit
11 and a flicker filter 12 and selects either of them according to
a control signal S and gives the selected signal to a format
conversion circuit 14. The selector 13 selects the RGB signal
outputted from the graphic controller and gives it to the format
conversion circuit 14 when a reproduced image of a DVD is outputted
to the television receiver in the full screen mode.

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Search Results - Record(s) 1 through 9 of 9 returned.

☐ 1. Document ID: US 6300932 B1

L34: Entry 1 of 9   File: USPT   Oct 9, 2001

US-PAT-NO: 6300932
DOCUMENT-IDENTIFIER: US 6300932 B1

TITLE: Electrophoretic displays with luminescent particles and materials for making the same

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC |
| Draw Desc | Image |

☐ 2. Document ID: US 6262706 B1

L34: Entry 2 of 9   File: USPT   Jul 17, 2001

US-PAT-NO: 6262706
DOCUMENT-IDENTIFIER: US 6262706 B1

TITLE: Retroreflective electrophoretic displays and materials for making the same

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC |
| Draw Desc | Image |

☐ 3. Document ID: US 6249271 B1

L34: Entry 3 of 9   File: USPT   Jun 19, 2001

US-PAT-NO: 6249271
DOCUMENT-IDENTIFIER: US 6249271 B1

TITLE: Retroreflective electrophoretic displays and materials for making the same

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC |
| Draw Desc | Image |

☐ 4. Document ID: US 6120839 A

L30	L28 and (reset$ near7 display$)	16	L30
L29	L28 and (first near3 driv$)	8	L29
L28	L24 and control$ and prevent$ and reset$	92	L28
L27	L24 and (control$ near9 (prohibit$ near3 driv$))	0	L27
L26	L24 and ((control$ near9 restrain$) same reset$)	0	L26
L25	L24 and ((control$ near9 prevent$) same reset$)	0	L25
L24	(e$book or electronic book or portable display)	1167	L24
L23	L22 and (first near5 driv$)	8	L23
L22	L12 and control$ and prevent$ and reset$	27	L22
L21	L12 and ((control$ near9 prevent$) same reset$)	0	L21
L20	L12 and ((control$ near9 restrain$) same reset$)	0	L20
L19	L12 and ((control$ near9 prohibit$) same reset$)	0	L19
L18	L12 and (control$ near9 (prohibit$ near3 driv$))	0	L18
L17	L12 and (control$ near9 (inhibit$ near3 driv$))	0	L17

DB=JPAB,EPAB,DWPI,TDBD; PLUR=YES; OP=ADJ

L16	L13 and driv$ and power	0	L16
L15	L13 and driv$ and power and read$ and write and control$	0	L15
L14	L13 and driv$ and power and read and write and control$ and reset$	0	L14
L13	(e-book or electronic book or portable display or micro$display) and 345/$.ccls.	11	L13

DB=USPT; PLUR=YES; OP=ADJ

L12	(e-book or electronic book or portable display or micro$display) and 345/$.ccls.	260	L12
L11	L10 and lcd	5	L11
L10	L9 and power and (control$ same reset$)	13	L10
L9	L8 and (first near4 driv$) and (second near4 driv$)	105	L9
L8	(e-book or electronic book or palm$top or organiz$ or portable display or micro$display) and 345/$.ccls.	5201	L8
L7	(e-book or electronic book or palm$top or organiz$ or portable display or micro$display)	108827	L7
L6	L4 and (inhibit$ near9 driv$)	1	L6
L5	L4 and (prevent$ near7 driv$)	0	L5
L4	L3 and (power same driv$) and (control$ near20 reset$)	31	L4
L3	L2 and driv$ near9 read$	147	L3
L2	L1 and driv$ near9 writ$	216	L2
L1	(e-book or electronic book or information display) and (information near8 stor$)	5329	L1

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US 0953833907P1

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Team: OIPEBackFileIndexing
Dossier: 09538339

Legal Date: 09-12-2002

No.	Doccode	Number of pages
1	A...	1
2	CLM	4
3	REM	10
4	XT/	2
5	LET.	1

Total number of pages: 18

Remarks:

Order of re-scan issued on ....